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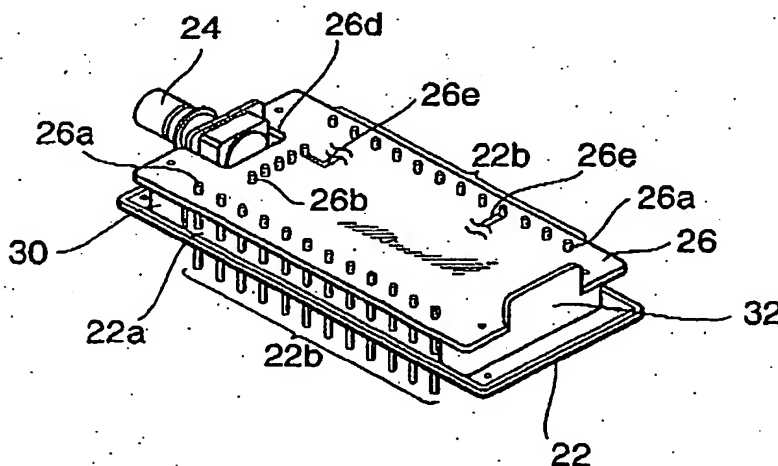
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(54) Opto-electronic module with printed circuit board (PCB)

(57) An optical data link 20 comprises a mounting member 22, an optical element assembly 24, a circuit board 26, and a spacer 32. The PGA substrate 22 has a substrate 22a and a plurality of electrically conductive pins 22b. The circuit board 26 has a pair of surfaces. Electronic components 34a to 34e are mounted on the pair of surfaces. The optical element assembly 24 in-

cludes a semiconductor optical element 24f. The semiconductor optical element 24f is connected to an electrically conductive layer on the circuit board 26. The spacer 32 functions so as to distance the circuit board 26 from the PGA substrate 22. Since the circuit board 26 is distanced from the PGA substrate 22, the electronic components 34a to 34e can be mounted on both surfaces of the circuit board 22.

Fig.2



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Description

BACKGROUND ART OF THE INVENTION

Field of the Invention

[0001] The present invention relates to an optical data link.

Related Background Art

[0002] An optical data link has a photoelectric transducer unit, a housing base member made of metal, a substrate directly secured onto this housing base member, and a cover member covering this substrate.

SUMMARY OF THE INVENTION

[0003] The inventor has been studied an optical data link as shown in Figs. 15A, 15B, 16A, and 16B. In this optical data link, a printed circuit board 4 and a photoelectric transducer unit 6 are mounted on a housing base member 2 made of metal. The printed circuit board 4 is provided with lead terminals 8 supported by the metal-made housing base member 2. The printed circuit board 4 is covered with a cover 10, and the cover 10 is attached to the housing base member 2. The optical data link 12 as shown in Fig. 15B is constituted by these components.

[0004] Referring to Fig. 16A, the circuit board 4 is directly mounted on the housing base member 2 in the optical data link 12. The circuit board 4 is bonded by silver paste applied to the rear face of the circuit board 4. The whole rear face is utilized for the bonding by use of the silver paste. On the only one side of the circuit board 4, electronic components are mounted. In order to mount more electronic components on the circuit board 4, the circuit board 4 has been made greater to obtain additional area for mounting components.

[0005] Referring to Fig. 16B, the housing base member 2 of the optical data link 12 comprises a plurality of components such as a covar-made component 2a, a CuW-made component 2b, and a covar-made component 2c. These components 2a, 2b, 2c are assembled by silver brazing. The optical data link is composed of these components, so that the assembling of the optical data link is complicated. It is not easy to downsize the optical data link because of the large number of the components and the structural complexity of the optical data link.

[0006] In the development of the optical data link of this type, the inventor has found that the optical data link will hereafter be required to mount a greater number of electronic components than those currently mounted without increasing the size of optical data link.

[0007] Therefore, it is an object of the present invention to provide an optical data link having a structure capable of increasing area for mounting more electronic

components thereon.

[0008] One aspect of the present invention relates to the optical data link. The optical data link comprises a mounting member, a circuit board, an optical element assembly, and support means. The mounting member has a substrate and a plurality of electrically conductive pins. The substrate has a plurality of holes. The electrically conductive pins pass through the plurality of holes of the substrate. The circuit board has a pair of surfaces. On each of the pair of surfaces, the circuit board mounts electronic components. The circuit board has, on each surface thereof, an electrically conductive layer connected to at least one of the plurality of electrically conductive pins. The optical element assembly includes a semiconductor optical element and is mounted on the mounting member. The semiconductor optical element is connected to the electrically conductive layer on the circuit board. The support means functions so as to distance the circuit board from the mounting member.

[0009] Another aspect of the present invention relates to the optical data link. The optical data link comprises a mounting member, a circuit board, an optical element assembly, and a support member. The support member is provided between the mounting member and the circuit board, and supports the circuit board so as to be spaced apart from the mounting member. Since the circuit board is spaced apart from the mounting member, both surface of the circuit board can be used for mounting the electronic components.

[0010] Since the circuit board is distanced from the mounting member, electronic components can be mounted on both surfaces of the circuit board.

[0011] The following features relating to the present invention can be combined with the above-mentioned invention. These features can be also combined with each other arbitrarily, whereby the invention obtained by these combinations can attain the operations and effects arising from the individual features as well as the combinations.

[0012] The optical data link may further comprise an electrically conductive cover covering the circuit board. One of the pair of surfaces of the circuit board faces a main surface of the mounting member. The mounting member has an electrically conductive film provided on the main surface. In this configuration, the circuit board is arranged between the electrically conductive cover and the electrically conductive film of the mounting member. This arrangement can improve the noise immunity of the optical data link.

[0013] In the optical data link, the mounting member may have an opening for accommodating an electronic component mounted on one of the surfaces of the circuit board. If the head of the electronic component is inserted into the opening, then the distance between the circuit board and the mounting member can be made smaller.

[0014] In the optical data link, both the mounting member and the circuit board may be arranged along a

reference plane. The electrically conductive cover has a plurality of opening portions arranged so as to communicate with accommodating space between the mounting member and the circuit board. Each opening portion works as vent for communicating space between the mounting member and the circuit board to the outside of the optical data link.

[0015] In the optical data link, a support member may be mounted on the mounting member so as to support the optical element assembly and the circuit board. The circuit board is distanced from the mounting member by use of the support member supporting the optical element assembly.

[0016] In the optical data link, the circuit board may have a plurality of holes into which the electrically conductive pins are inserted. The circuit board is supported by at least one of the electrically conductive pins. The electrically conductive pins are provided so as to distance the circuit board from the mounting member.

[0017] In the optical data link, the mounting member may comprise an insulating member provided along a surface of the mounting member, and this surface is opposed to the main surface of the mounting member. The insulating member can electrically insulate the optical data link from a printed circuit board on which the optical data link is mounted.

[0018] In the optical data link, one side of the circuit board has a depressed portion. The optical element assembly may be arranged in this depressed portion. The depressed portion allows the length of the optical data link to decrease by a length corresponding to the depth of the depressed portion.

[0019] In the optical data link, one side of the circuit board has a depressed portion, whereas one side of the substrate of the mounting member has a depressed portion. The optical element assembly may be provided in these depressed portions. This configuration of the optical data link does not require the optical data link to have a sufficient height to accommodate the optical element assembly between the mounting member and the circuit board. Therefore, the height of the optical data link is reduced.

[0020] In the optical data link, the optical element assembly has an optical element mounting member, a plurality of terminals, a lens holding member, a lens, and a guide member. The following members may be arranged in the direction of a predetermined axis: the circuit board; an optical element mounting member; a semiconductor optical element; a lens holding member; a lens; and a guide member. The optical element mounting member mounts the semiconductor optical element. The optical element mounting member holds the terminals so as to be directed to the direction of a predetermined axis. The lens holding member is placed on the optical element mounting member so as to cover the semiconductor optical element. The lens is held in the lens holding member. The guide member is provided on the lens holding member.

[0021] Still another aspect of the present invention is the optical data link. The optical data link further comprises a semiconductor chip, a protection member, and a heat transfer part. The semiconductor chip is mounted on the circuit board spaced apart from the mounting member. The protection member exhibits electrical insulation and protects the semiconductor chip. There are the following embodiments of the optical data link that has the above structure.

[0022] In the optical data link, the means may include a support member provided between the mounting member and the circuit board. The support member supports the circuit board so as to be spaced apart from the mounting member. The heat transfer part is provided so as to contact the protection member and the optical element assembly. The heat transfer part serves to diffuse heat generated in the semiconductor chip and the optical element assembly.

[0023] In one embodiment, the semiconductor chip is provided between the circuit board and the electrically conductive cover. The heat transfer part is provided so as to contact the optical element assembly, the protection and the electrically conductive cover. The heat transfer part conducts heat generated in the optical element assembly and the semiconductor to the electrically conductive cover.

[0024] In another embodiment, the semiconductor chip is provided between the circuit board and the mounting member. The heat transfer part is provided so as to contact the protection member, the optical element assembly and the mounting member. The heat transfer part conducts heat generated in the optical element assembly and the semiconductor to the mounting member.

[0025] In still another embodiment, the semiconductor chip is provided between the circuit board and the mounting member. The circuit board comprises a thermal via in an area in which the semiconductor chip is provided. The heat transfer member is provided so as to contact the thermal via, the optical element assembly and the electrically conductive cover. The heat transfer part serves to conduct heat generated in the optical element assembly and the semiconductor to the electrically conductive cover through the thermal via.

[0026] In the optical data link, the heat transfer part is provided so as to contact an electric conductor connected with said semiconductor chip. The heat transfer part can receive heat generated in the semiconductor chip through the electric conductor.

[0027] In the optical data link, the heat transfer part has electrical insulation preventing electrical conduction from occurring therethrough.

[0028] In the optical data link, the heat transfer part exhibits flexibility to deform so as to conform to the shape of the protection member and optical element assembly when the heat transfer part is provided between the electrically conductive cover and the protection member and optical element assembly. The heat transfer part is deformed so as to increase contacting area

between the heat transfer part and the protection member and optical element assembly. For example, the heat transfer part includes silicone gel material.

[0029] In the optical data link, the optical element assembly includes a semiconductor optical element. The semiconductor chip comprises circuit elements for driving said semiconductor optical element. The heat transfer part is used to conduct heat generated in the semiconductor chip and the optical element assembly to the outside of the optical data link.

[0030] In the optical data link, the protection member includes a package. The package houses the semiconductor chip.

[0031] In the optical data link, the protection member includes a resin. The resin is provided on the circuit board so as to cover the semiconductor chip therewith.

[0032] In the optical data link, the semiconductor chip is mounted on the circuit board by a flip-chip method.

[0033] The present invention will become more fully understood from the detailed description given hereinbelow and the accompanying drawings which are given by way of illustration only, and thus are not to be considered as limiting the present invention.

[0034] Further scope of applicability of the present invention will become apparent from the detailed description given hereinafter. However, it should be understood that the detailed description and specific examples, while indicating preferred embodiments of the invention, are given by way of illustration only, since various changes and modifications within the spirit and scope of the invention will become apparent to those skilled in the art from this detailed description.

BRIEF DESCRIPTION OF THE DRAWINGS

[0035] The above-mentioned object and other objects, features, and advantages of the present invention will be understood more easily from the following detailed description of preferred embodiments of the present invention set forth with reference to the accompanying drawings.

Fig. 1 is a view showing the appearance of the optical data link in accordance with an embodiment of the present invention;

Fig. 2 is a view showing the inner structure of the optical data link in accordance with the embodiment of the optical data link;

Fig. 3 is a view showing a mounting member for the optical data link;

Fig. 4 is a view showing a side of the optical data link shown in Fig. 2;

Fig. 5 is a cross sectional view, taken along the line I-I shown in Fig. 1, showing the optical data link;

Fig. 6 is a view showing the appearance of the optical data link in accordance with another embodiment;

Fig. 7 is a view showing the optical data link in ac-

cordance with still another embodiment;

Fig. 8 is a perspective view showing an optical data link according to the fourth embodiment;

Fig. 9 is a view showing an internal structure of the optical data link according to the fourth embodiment;

Fig. 10 is a cross sectional view, taken along the line II-II shown in Fig. 8, showing an optical data link;

Fig. 11 is a plan view showing a circuit board for the optical data link shown in Fig. 8;

Fig. 12A is a cross sectional view, taken along a line III-III shown in Fig. 8, showing the optical data link;

Fig. 12B is a cross sectional view, taken along a line equivalent the line III-III, showing an optical data link;

Fig. 12C is a cross sectional view, taken along a line equivalent to the line III-III, showing an optical data link;

Fig. 13 is a cross sectional view, taken along a cross sectional line equivalent to the line II-II shown in Fig. 8, showing an optical data link;

Fig. 14 is a cross sectional view, taken along a line equivalent to the line II-II shown in Fig. 8, showing an optical data link;

Figs. 15A and 15B are views showing an optical data link for comparison with the optical data link of the present invention;

Fig. 16A is a view showing an optical data link for comparison with the optical data link of the present invention; and

Fig. 16B is a view showing sheet metal parts for the optical data link for comparison with the optical data link of the present invention.

DESCRIPTION OF THE PREFERRED EMBODIMENT

[0036] The optical data links in accordance with embodiments of the present invention will be explained with reference to the drawings. Parts identical to each other will be referred to with reference numerals or letters identical to each other, if possible.

(First Embodiment)

[0037] Fig. 1 is a external view showing an optical data link in accordance with the embodiment, whereas Fig. 2 is a view showing the inner structure of the optical data link. Figs. 1 and 2 show an optical data link 20. The optical data link 20 comprises a mounting member 22, an optical element assembly 24, a circuit board 26, and a cover member 28. The circuit board 26 is provided on the mounting member 22. The cover member 28 is provided on the mounting member 22. The the circuit board 26 is provided between the mounting member 22 and the cover member 28. The optical element assembly 24 is supported by a first support member 30, such as a spacer. The first support member 30 is mounted on the main surface of the mounting member 22. According to

this arrangement, the optical element assembly 24 is positioned on the mounting member 22 so as to be directed to a predetermined axial direction. The circuit board 26 can be supported by a second support member 32, such as a spacer. The second support member 32 is mounted on the main surface of the mounting member 22. This structure provides additional space for arranging electronic components between the circuit board 26 and the main surface of the mounting member 22. Also, the circuit board 26 may be supported by the first support member 30. The second support member 32 is provided away from the first support member. In an example as shown in Fig. 2, the first support member 30 is disposed to face one of a pair of sides of the mounting member 22, whereas the second support member 32 is disposed to face the other of the pair of the sides of the mounting member 22. These sides intersect the predetermined axis.

[0038] Fig. 3 shows the mounting substrate. An example of mounting member 22 is a PGA (pin grid array) substrate. The mounting member 22 has a substrate 22a and electrically conductive pins 22b. The substrate 22a has a plurality of holes through which the electrically conductive pins 22b pass. The electrically conductive pins 22b are arranged in a pair of lines extending in the direction of a predetermined axis. These lines are arranged along the respective sides of the mounting member. These sides extend in the direction of the predetermined axis.

[0039] An electrically conductive film 22c is provided substantially over the whole main surface of the substrate 22a. The electrically conductive film 22c is connected to one pin of the electrically conductive pins provided to connect with a reference potential line. The substrate 22a has, at one side thereof, positioning holes 22e defining the position of the first support member 30.

[0040] Also, the substrate 22a has a cutout 22d provided so as to accommodate the tail of the optical element assembly 24. The optical element assembly 24 is supported by the first support member 30 positioned with respect to the positioning holes 22e while the optical element assembly 24 is disposed within the cutout 22d. The substrate 22a has positioning holes 22h, along one side of the substrate 22a, for defining the position of the second support member 32. The substrate 22a has one or a plurality of opening portions 22f and 22g. The positions of opening portions 22f and 22g are aligned with positions of electronic components mounted on the rear of the circuit board 26. The substrate 22a comprises through holes 22i for positioning the electrically conductive cover member 28. As can be seen from this explanation, the main parts are positioned with respect to the substrate 22a.

[0041] Referring to Fig. 2 again, the circuit board 26 has a plurality of through holes 26a into which the electrically conductive pins 22b are inserted. The circuit board 26 has through holes 26b into which the terminals of the optical element assembly 24 are inserted. The op-

tical element assembly 24 is held with the support member 30 while the terminals of the optical element assembly 24 are inserted in the through holes 26b. Although not totally illustrated, wirings are provided on the circuit board 26 and are made of electrically conductive layers 26c. The wirings extend to the surroundings of the plurality of through holes 26a and the through holes 26b. The electrically conductive pins 22b are inserted into the through holes 26a and the terminals of the optical element assembly 24 are inserted in the through holes 26b. These electrically conductive layers 26c are provided on both surfaces of the circuit board 26, respectively.

[0042] One side of the circuit board 26 is provided with a cutout 26d provided so as to extend in the direction of the predetermined axis. The cutout 26d is provided so as to accommodate the tail of the optical element assembly 24. The optical element assembly 24 is provided within the cutout 26d and is connected to the electrically conductive layers 26c of the circuit board 26.

[0043] Fig. 4 is a side view showing the optical data link with the cover member 28 removed therefrom. In the optical data link 20, the optical element assembly 24 is provided within both cutouts 26d and 22d. This configuration of the optical element assembly 24 allows the reduction of the height L_1 of the optical data link 20, and allows the reduction of the length L_2 of the optical data link 20 in the direction of a predetermined axis.

[0044] Referring to Fig. 4, electronic components 34a to 34e are arranged on a pair of surfaces 26f and 26g of the circuit board 26. The electronic components 34a and 34b are mounted on the first mounting surface 26f of the circuit board 26. The electronic components 34a and 34b are electrically connected to the optical element assembly 24 or electrically conductive pins 22b by way of an electrically conductive layer 26e. The electronic components 34c to 34e are mounted on the second mounting surface 26g of the circuit board 26. The electronic components 34c to 34e are electrically connected to the optical element assembly 24 or electrically conductive pins 22b by way of the electrically conductive layer 26e. The heights of the electronic components 34c, 34d and 34e are higher than those of the electronic components 34a and 34b. This configuration allows the reduction of the distance between the circuit board 26 and the cover member 28, so that the height L_1 of the optical data link 20 can be reduced. The head of the electronic component 34c is inserted in the opening portion 22f of the substrate 22a, whereas the head of the electronic component 34e is inserted in the opening portion 22g of the substrate 22a. Thus, the heights of electronic components 34c and 34e are not restricted by the interval between the circuit board 26 and the substrate 22, and the height L_1 of the optical data link 20 can be reduced.

[0045] Fig. 5 is a sectional view taken along the line I-I of Fig. 1 and shows the optical data link 20. A positioning portion 28b of the electrically conductive cover member 28 is inserted into a hole 22i of the substrate

22a, whereby the cover member 28 is positioned and secured to the mounting member 22. The optical element assembly 24 and the electronic components 34a to 34e are covered with the electrically conductive cover member 28, whereas the electrically conductive film 22c is provided on the main surface of the substrate 22. These electrically conductive parts shield the optical element assembly 24 and the electronic components 34a to 34e from external noises.

[0046] The optical element assembly 24 comprises: an optical device mounting member 24a, such as a stem; a lens holding member 24b; a lens 24c held with the lens holding member 24b; and a guide member 24d, such as a sleeve. The optical element mounting member 24a, lens holding member 24b, lens 24c, and guide member 24d are arranged in the direction of a predetermined axis. One end of the lens holding member 24b is provided on a surface of the optical element mounting member 24a. The guide member 24d is provided on the other end of the lens holding member 24b. The guide member 24d defines a direction in which an optical coupling device, such as an optical connector, is to be inserted. A mounting portion 24e for mounting a semiconductor optical element 24f projects from the preparing surface of the optical element mounting member 24a. The semiconductor optical element 24f is placed on the mounting portion 24e so as to align with the direction of the predetermined axis, so that the semiconductor optical element 24f is aligned with the optical axis of the optical data link 20. The semiconductor optical element 24f is electrically connected to a terminal 24g provided in the optical element mounting member 24a. The optical data link of this configuration allows the electrical connection of the semiconductor optical element 24f to the electronic components 34a to 34c arranged on the circuit board 26.

[0047] If the semiconductor optical element 24f is a semiconductor light emitting device such as a semiconductor laser, the optical data link 20 acts as a transmitter data link. If the semiconductor optical element 24f is a semiconductor light receiving device such as a photodiode, the optical data link 20 acts as a receiver data link. In the optical data link 20, an optical connector 36 is inserted along the guide member 24d in the direction of arrow A. The optical connector 36 comprises a ferrule 36a and an optical fiber 36b, one end of which appears at one end of the ferrule 36a.

[0048] In the optical element assembly 24, the optical element mounting member 24a, lens holding member 24b, and guide member 24d are made of an electrically conductive material such as metal. The optical element assembly 24 is disposed so as to come into contact with the electrically conductive layer 22c and then is electrically connected to the electrically conductive layer 22c of the substrate 22. Further, the optical element assembly 24 comprises a connecting portion, such as an electrically conductive protrusion 24h. The protrusion 24h is provided on a side surface of the optical element assembly

24 so as to come into contact with the electrically conductive cover member 28. The optical data link of this configuration allows the electrical connection of the housing of the optical element assembly 24 to the electrically conductive cover member 28. The electrically conductive protrusion 24h of the optical element assembly is effective in improving the noise immunity of the optical data link 20.

[0049] The second support member 32 has an electrically conductive portion 32a, such as a metal plate and a metal pin. The electrically conductive portion 32a is provided so as to electrically connect the electrically conductive film 22c and the electrically conductive cover member 28 to each other when the second support member 32 is mounted on the mounting member 22. The optical data link of this configuration allows the electrical connection of the electrically conductive member 28 to the electrically conductive film 22c of the substrate 22. The electrically conductive portion 32a of the second support member 32 improves the noise immunity of the optical data link 20.

[0050] The first support member 30 may have an electrically conductive portion, such as a metal plate and a metal pin, whose shape is equivalent or similar to that of the second support member 32. This electrically conductive portion is provided so as to connect the electrically conductive film 22c with the electrically conductive cover member 28 when the first support member 30 is placed on the mounting member 22. This improves the noise immunity of the optical data link 20. The first support member 30 comprises an electrically conductive portion for connecting the housing of the optical element assembly 24 with the electrically conductive film 22c when the optical data link 20 is placed on the first support member 30.

(Second Embodiment)

[0051] Fig. 6 is a perspective view showing the optical data link in accordance with another embodiment. This optical data link 40 further comprises an insulating member 38, such as an insulating sheet. The insulating member 38 is provided along the rear face of the substrate 22. The insulating member 38 can prevent unpredictable electric short-circuits from occurring in mounting the optical data link 20 on a printed circuit board 50.

(Third Embodiment)

[0052] Fig. 7 is a side view showing the optical data link in accordance with still another embodiment. In this optical data link 44, the cover member 28 has one or a plurality of opening portions 28a. In the example shown in Fig. 7, the opening portions 28a are arranged in sides of the cover member 28. The opening portions 28a enable the ventilation with the inside of the optical data link 44. In this configuration, heat is generated from the electronic components and optical element assembly and

can be released efficiently through the openings. The position of the opening portions 28a are aligned with a position between the substrate 22 and the circuit board 26. Even when the mounting density of electronic components is enhanced by the double-sided mounting, the heat from the electronic components can be released. Further, the opening portions 28a may be arranged in the direction of the predetermined axis and each opening portion may have a shape extending in the direction. In this configuration, the generated heat can efficiently be released through a region between the substrate 22 and the circuit board 26.

[0053] As explained in detail with reference to the drawings in the foregoing, mounting areas for electronic components are provided on both sides of the circuit board. As a consequence, these electronic components can be mounted on both surfaces of the circuit board. Therefore, the optical data link of this configuration allows the downsizing of the circuit board, and is effective in making the optical data link smaller.

[0054] Without using a sheet metal component as the housing base of the optical data link, the mounting member equipped with electrically conductive pins is used. The structure of the mounting member is simpler than that of the sheet metal, and is effective in reducing the cost of the optical data link.

[0055] The mounting substrate has an electrically conductive film therein or thereon and this film can block external noises. The optical data link made by the inventor has yielded noise immunity characteristic identical to or higher than the optical data link manufactured with a sheet metal component.

(Fourth Embodiment)

[0056] Fig. 8 is a perspective view showing an optical data link in accordance with still another embodiment. Fig. 9 is a view showing an inner structure of the optical data link according to the present embodiment. Referring to Figs. 8 and 9, the optical data link 20b is shown. The optical data link 20b comprises a mounting member 22, an optical element assembly 25, a circuit board 26, a cover member 28, and a heat transfer part 60. In the optical data link 20b, a guide member 52 such as a sleeve is disposed at the head of the optical element assembly 25. The guide member 52 is capable of guiding the ferrule 56 attached on one end portion of the optical fiber 54, and enables the optical connection between the end of the optical fiber 54 and the optical element assembly 25.

[0057] The optical data link 20b comprises an optical element assembly 25 instead of the optical element assembly 24. The structure of the optical element assembly 25 may be the same as that of the optical element assembly 24 shown in Fig. 5, but is not limited thereto. The optical element assembly 25 is supported with a first support member 30. The first support member 30 is mounted on the principal surface of the mounting mem-

ber 22, and positions the optical element assembly 25 on the mounting member 22 such that the optical element assembly 25 is directed in a direction of the predetermined axis. In the optical element assembly 25, terminals 25g, such as lead terminals, extend from the optical element assembly 25 along the circuit board 26, and are bent so as to pass through the through hole 26b, so that the end of the terminal 25g faces the mounting substrate 22.

[0058] For example, when the optical data link 20b works as a transmission data link, the electronic device 62 operates so as to generate a signal provided to a semiconductor light-emitting element such as a semiconductor laser element (for example, reference numeral 24e of Fig. 5 within the optical data link 20b). When the optical data link 20b works as a receiving data link, the electronic device 62 operates so as to amplify signal provided from a semiconductor light-detecting element, such as a photodiode (for example, reference numeral 24e of Fig. 5 within the optical data link 20b).

[0059] The circuit board 26 is supported by a second support member 32. The second support member 32 is disposed on the principal surface of the mounting member 22, so that a certain amount of space for arranging the electronic components is provided between the circuit board 26 and the principal surface of the mounting member 22. Moreover, the circuit board 26 may also be supported by the first support member 30. The first support member 30 is disposed on the principal surface of the mounting member 22. In this embodiment, the first support member 30 and the second support member 32 are arranged between the mounting member 22 and the circuit board 26, and define the gap between the mounting member 22 and circuit board 26.

[0060] The heat transfer part 60, as shown in Fig. 8, is positioned between the circuit board 26 and the conductive cover 28. As shown in Fig. 9, an electronic device 62 is disposed in contact with the bottom of the heat transfer part 60. The heat transfer part 60 is disposed on the circuit board 26 and is provided so as to contact the conductive cover 28 as well as to contact the conductive portion of the optical element assembly 25 and the electronic device 62. The heat transfer part 60 has a pair of faces, mutually opposed with each other, one of which realizes this contact. One of these faces, a face 60a, has an area sufficient to cover a region (dashed box shown in Fig. 9 for example) in which the electronic device 62 and the conductive pin 25g of the optical element assembly 25 are arranged. The other face, a face 60b, is provided so as to contact the inner wall of the conductive cover 28. The heat transfer part 60 serves to conduct heat from the electronic device 62 and the optical element assembly 25 to the conductive cover 28. Moreover, the heat transfer part 60 comprises side faces 60c to 60f, which are also capable of releasing the heat into the air.

[0061] In addition, the heat transfer part 60 has a thickness equivalent to or slightly larger than the dis-

tance between the circuit board 26 and the conductive cover 28. Moreover, it is preferable that the heat transfer part 60 exhibit a certain degree of flexibility sufficient to be compressed to deform between the conductive cover 28 and the electronic device 62, and optical element assembly 25. The flexibility allows the compressed heat transfer part 60 to conform to the irregular shape of the electronic device 62 and the conductive pin 25g.

[0062] The heat transfer part 60 has advantages arising from the foregoing behavior. The deformed heat transfer part 60 generates stress therein which ensures that heat transfer part 60 contacts the electronic device 62 and the optical element assembly 25 and that the area of the contact can be increased. Further, since the heat transfer part 60 deforms to conform to the external shape of the electronic device 62 and the optical element assembly 25, it is not necessary to make the heat transfer part 60 having its desired shape. In addition, the heat transfer part 60 exhibits electrical insulation such that electrical conduction will not occur between the optical element assembly 25 and the conductive cover 28 therethrough.

[0063] Because of the deformation of the heat transfer part 60, the heat transfer part 60 closely contacts the electronic device 62 and optical element assembly 25. The heat transfer part 60 causes heat from the electronic device 62 and the optical element assembly 25 to disperse and spread. The contact area of the heat transfer part 60 with the conductive cover 28 is larger than that of the electronic device 62 and optical element assembly 25 with the heat transfer part 60. This larger contact surface allows the heat to efficiently conduct to the conductive cover 28 and to be released.

[0064] Preferably, the heat transfer part 60 exhibits adhesiveness to facilitate the maintenance of the contact between the heat transfer part 60 and components contacted therewith, such as the electronic device 62, the optical element assembly 25 and the conductive cover 28.

[0065] The present inventors think as follows: the thermal conductivity of material for the heat transfer part 60 is preferably $2.0 \text{ W/m} \cdot \text{K}$ or more; and a silicone gel can be exemplified as material for the heat transfer part 60.

[0066] Referring to Figs. 8 and 9, the circuit board 26 comprises wiring layers 26h, 26i and 26j connected to the electronic device 62. It is preferable that the heat transfer part 60 is in contact with at least one layer of these wiring layers 26h, 26i and 26j. If the heat transfer part 60 contacts the wiring layers 26h and 26j, the wiring layers 26h and 26j conducts heat from the electronic device 62 to the heat transfer part 60. Moreover, the wiring layer 26i on the circuit board 26 is connected to the conductive pin 25g of the optical element assembly 25 and the electronic device 62. If the heat transfer part 60 contacts this wiring layer 26i, the wiring layer 26i provides heat from both the optical element assembly 25 and the electronic device 62 with the heat transfer part 60. Thus,

the thermal interference between the optical element assembly 25 and the electronic device 62 is reduced.

[0067] Fig. 10 is a cross sectional view, taken along line II-II illustrated in Fig. 8, showing the optical data link 20b. In this optical data link 20b, the heat transfer part 60 is provided so as to contact the upper and side surfaces of the electronic device 62. Fig. 11 is a plan view showing the circuit board 26 used in the optical data link 20b. In Fig. 11, the heat transfer part 60 is provided within an area shown by a broken line. The heat transfer part 60 is positioned so as to contact conductors such as the wiring layer 26h, 26i and/or 26j connected to the electronic device 62 while covering the electronic device 62. Further, the heat transfer part 60 is positioned so as to contact one or more conductors such as the conductive terminal 25g of the optical element assembly 25.

[0068] Fig. 12A is a cross sectional view, taken along the line III-III illustrated in Fig. 8, showing the optical data link. Fig. 12A shows the details of the electronic device 62. The electronic device 62 comprises a lead frame 62a, a semiconductor chip 62b, and a molded resin body 62c. In the electronic device 62, a semiconductor chip 62b is disposed on the lead frame 62a, and the molded resin body 62c covers the semiconductor chip 62b.

[0069] This electronic device 62 is mounted on the circuit board 26. A part of the lead frame 62a extends from the molded resin body 62c to form the external lead terminals 62d. The external lead terminals 62d are connected to wiring layers 26h and 26i, provided on the circuit board 26, via conductive connection members such as a solder.

[0070] The heat transfer part 60 is deformed to conform to the shapes of the molded resin body 62c, external lead terminals 62d, conductive layers 26i and 26h, and optical element assembly 25 (for example, lead terminal 25g). In other words, depressed portions formed on the heat transfer part 60 correspond to the shapes of the conductive layers 26i and 26h on the circuit board 26, the external lead terminal 62d and the optical element assembly 25 (e.g., lead terminals 25g), respectively. The formation of this depressed portions enables the heat transfer part 60 to contact each of the components 62c, 62d, 25, 26i and 26h.

[0071] Fig. 12B is a cross section, taken along a line equivalent to the line III-III illustrated in Fig. 8, showing the optical data link according to another embodiment. Fig. 12B shows the detail of the electronic device 64 included in the optical data link of another embodiment. The electronic device 64 comprises an electrode 64a, such as a metal bump, provided on the wiring layers 26h and 26i, a semiconductor chip 64b provided on the electrode 64a, and a sealant resin portion 64c for covering the semiconductor chip 64b and the electrode 64a. In this embodiment, the semiconductor chip 64b is mounted in a flip-chip configuration, and the pad electrode on the semiconductor chip 64b is connected to the wiring layers 26i and 26h via the electrode 64a. After mounting the semiconductor chip 64a on the circuit board 26, the

sealant resin portion 64c is provided by potting sealant resin material on the semiconductor chip 64a on the circuit board 26 and solidifying it.

[0072] The heat transfer part 60 is deformed to conform to the shape of the sealant resin portion 64c, the conductive layers 26i and 26h, and the lead terminal 25g. In other words, this deformation creates depressions on the heat transfer part 60 and the depressions correspond to the shape of the conductive layers 26h and 26j on the circuit board 26, the sealant resin portion 64c and the lead terminals 25g. The depressions enable the heat transfer part 60 to contact each of components 64c, 26h, 26i and 25.

[0073] Fig. 12C is a cross sectional view, taken along a line equivalent to the line III-III illustrated in Fig. 8, showing the optical data link of another embodiment. Fig. 12C shows the details of the electronic device 66 included in the optical data link of another embodiment.

[0074] The electronic device 66 comprises a semiconductor chip 64a, a wiring member 66b and a sealant resin portion 66c. The semiconductor chip 64a is provided on the circuit board 26. The wiring members 66b connect the pad electrodes on the semiconductor chip 64a with the wiring layers 26h and 26i, for example, by wire bonding. The sealant resin portion 66c covers the semiconductor chip 66a and the wiring members 66b.

[0075] In this embodiment, the semiconductor chip 66a has a metal layer on the face (back side) opposed to the front face on which active elements, such as transistors, are provided, and this metal layer on the back side is bonded with the metal layer 26k on the circuit board via an adhesive member 66d. After the semiconductor chip 66a is mounted on the circuit board 26 and the wire bonding is completed, the sealant resin portion 66c is formed by potting a sealant resin on the semiconductor chip 64a and the circuit board 26 and solidifying it.

[0076] The heat transfer part 60 is deformed to conform to the shapes of the sealant resin portion 66c, conductive layers 26i and 26h, and the lead terminal 25g. In other words, this deformation creates depressed portions formed on the heat transfer part 60, and the shapes of the depressions are associated with the shapes of the conductive layers 26i and 26h on the circuit board 26, the sealant resin portion 66c and the lead terminal 25g, respectively. The formation of this depressed portions enable the connection of the heat transfer part 60 with each of components 66c, 26i, 26h and 25.

[0077] In the respective embodiments illustrated in Figs. 12A to 12C, each of the sealant resin body 62c and the sealant resin portions 64c and 66c works as a protective member for protecting the semiconductor chip. Moreover, the heat transfer part 60 exhibits electric insulation to avoid the generation of a conductive path through the heat transfer part 60 among the optical element assembly 25, the conductive layers 26h, 26i, 26j and the conductive cover 28.

(Fifth Embodiment)

[0078] Fig. 13 is a cross sectional view, taken along a line equivalent to the line II-II illustrated in Fig. 8, showing the optical data link 20c. In the optical data link 20c, the electronic device 62 is positioned between the circuit board 26 and the mounting substrate 22, and is mounted on the mounting area on one face, face 26g, of the circuit board 26. The circuit board 26 comprises a plurality of thermal vias 26m provided to the mounting region. Each of said thermal vias 26m extends from one face of the circuit board 26 to the other face, and is made of material exhibiting a better thermal conductivity than that of the circuit board 26. The thermal vias 26m are provided in contact the rear face of the electronic device 62. Meanwhile, the heat transfer part 61 is provided between the wiring member 26 and the conductive cover 28, and is in contact with both the mounting region of the wiring member 26 and the conductive cover 28. Thus, heat is generated in the electronic device 62, and the heat is conducted to the conductive cover 28 via the thermal via 26m and the heat transfer part 61 and then is released from the conductive cover 28.

[0079] Moreover, the heat transfer part 61 is provided so as to contact conductors such as the conductive pins 25g of the optical element assembly 25. Thus, the heat transfer part 61 works to conduct the heat from the electronic device 62 and the optical element assembly 25 to the conductive cover 28.

[0080] It is preferable that the heat transfer part 61 exhibit a certain degree of flexibility sufficient to deform to conform to the shape of the optical element assembly 25 when disposed between the conductive cover 28 and the optical element assembly 25.

[0081] The heat transfer part 61 exhibits electric insulation so as to avoid the generation of a conductive path therethrough between the optical element assembly 25 and the conductive cover 28.

(Sixth Embodiment)

[0082] Fig. 14 is a cross sectional view, taken along a line equivalent to the line II-II illustrated in Fig. 8, showing the optical data link 20d. In the optical data link 20d, the electronic device 62 is mounted on the mounting region on one side, the face 26g, of the circuit board 26, and is positioned between the circuit board 26 and the mounting substrate 22. Meanwhile, the heat transfer part 60 is positioned between the wiring member 26 and the mounting member 22, and is in contact with the wiring member 26, electronic device 62 and mounting member 22. Thus, heat is generated in the electronic device 62 and the generated heat is conducted to the mounting member 22 via the heat transfer part 60, and then is released from the mounting member 22.

[0083] Moreover, the heat transfer part 60 is provided so as to contact the conductors of the optical element assembly 24, such as the conductive pins 24g. Further-

more, a wiring layer 26n is provided on the face 26g of the circuit board 26 and is connected to the electronic component 62. Thus, the heat transfer part 60 works to conduct the heat from the wiring layer 26n, the electronic device 62 and the optical element assembly 24 to the mounting member 22.

[0084] It is preferable that the heat transfer part 60 exhibit a certain degree of flexibility. Due to the flexibility, the heat transfer part 60 is capable of deforming to conform to the shapes of electronic device 62 and the optical element assembly 24 when disposed between the mounting member 22 and electronic device 62 and optical element assembly 24.

[0085] The heat transfer part 60 provides electric insulation so as to avoid the generation of a conductive path therethrough between the optical element assembly 24, conductive film 22c on the mounting member 22, and the wiring layer 26n on the circuit board 26.

[0086] As described above, the heat transfer part is capable of providing one or more heat-generating sources within the optical data link with a new heat-release path. The new path prevents components in the optical data link from exhibiting its thermal runaway. Thus, the new path allows the optical data links in the embodiments to operate in the wider range of the operating temperature. Further, the optical data link in the present embodiments show favorable transmission waveform even in high temperatures.

[0087] As described in the foregoing, the optical data link in accordance with the present invention has support means for supporting a circuit board so as to be away from a mounting member. Since the circuit board is distanced from the mounting member, electronic components can be mounted on both surfaces of the circuit board.

[0088] Therefore, an optical data link having a structure capable of increasing the area for mounting electronic components are provided.

[0089] Although the principle of the present invention is illustrated and explained in preferred embodiments, those skilled in the art can recognize that the present invention can be modified in its arrangement and details without departing from the principle. The circuit board is supported by the support member in the embodiments, but the circuit board may be supported by the electrically conductive pin while being distanced from the substrate of the mounting member, for example. Also, any structure allowing the circuit board to be distanced from the substrate of the mounting member can be employed without being limited to the specific configurations disclosed in the embodiments. Therefore, we claim rights for all the modifications and changes from the scope of claims and the spirit.

[0090] From the invention thus described, it will be obvious that the embodiments of the invention may be varied in many ways. Such variations are not to be regarded as a departure from the spirit and scope of the invention, and all such modifications as would be obvious to one

skilled in the art are intended for inclusion within the scope of the following claims.

Claims

1. An optical data link comprising:

a mounting member having a substrate with a plurality of holes, and a plurality of electrically conductive pins passing through said plurality of holes of said substrate;
a circuit board having a pair of surfaces with an electrically conductive layer connected to at least one pin of said plurality of electrically conductive pins, each of said pair of surfaces mounting an electronic component;
an optical element assembly, mounted on said mounting member, including a semiconductor optical element connected to said electrically conductive layer provided on said circuit board;
and
means for supporting said circuit board so as to be spaced apart from said mounting member.

2. The optical data link according to claim 1, further comprising an electrically conductive cover, said cover covering said circuit board,

wherein said circuit board is provided such that one of said pair of surfaces thereof faces a main surface of said mounting member, and

wherein said mounting member has an electrically conductive film on said main surface of said mounting member.

3. The optical data link according to claim 1, wherein said mounting member has a hole for accommodating a part of said electronic component mounted on one of the surfaces of said circuit board.

4. The optical data link according to claim 2, wherein said mounting member and said circuit board are arranged along a reference plane,

wherein said electrically conductive cover has a plurality of opening portions provided so as to communicate with space between said mounting member and said circuit board.

5. The optical data link according to claim 1, wherein said optical element assembly and said circuit board are supported by a support member mounted on said mounting member.

6. The optical data link according to claim 1, wherein said circuit board has a plurality of holes, said plurality of conductive pins being inserted into a plurality of holes.

7. The optical data link according to claim 2, wherein said mounting member has an insulating member provided along a surface of said mounting member, said surface of said mounting member being opposed to said main surface of said mounting member. 5
8. The optical data link according to claim 1, wherein said optical element assembly has a device mounting member, a terminal, a lens holding member, a lens, and a guide member, said device mounting member mounting said semiconductor optical element, said terminal being held with said element mounting member, said lens holding member being provided on said device mounting member so as to cover said semiconductor optical element, said lens being held by said lens holding member, and said guide member being provided on said lens holding member; 10
- wherein said circuit board, said device mounting member, said semiconductor optical element, said lens holding member, said lens, and said guide member are arranged in the direction of a predetermined axis. 15
9. The optical data link according to claim 1, wherein one edge of said circuit board has a depressed portion, said optical element assembly being provided in said depressed portion. 20
10. The optical data link according to claim 1, wherein said circuit board comprises an edge having a depressed portion; 25
- wherein said substrate of said mounting member comprises an edge having a depressed portion; and 30
- wherein said optical element assembly is arranged in the depressed portions of said circuit board and substrate. 35
11. The optical data link according to any one of claims 1 to 10, further comprising: 40
- a semiconductor chip mounted on said circuit board; 45
- an electrically insulating protection member covering said semiconductor chip; and
- a heat transfer part provided so as to contact said protection member and said optical element assembly. 50
- wherein said means includes a supporting member provided between said circuit board and said mounting member, and 55
- wherein said supporting member supports said circuit board so as to separate said circuit board from said mounting member.
12. The optical data link according to any one of claims 1 to 10, further comprising:
- an electrically conductive cover covering said circuit board;
- a semiconductor chip mounted on said circuit board;
- an electrically insulating protection member covering said semiconductor chip; and
- a heat transfer part provided so as to contact said protection member, said optical element assembly, and said electrically conductive cover;
- wherein said wiring semiconductor chip is provided between said circuit board and said electrically conductive cover.
13. The optical data link according to any one of claims 1 to 10, further comprising:
- an electrically conductive cover covering said circuit board;
- a semiconductor chip mounted on said circuit board;
- an electrically insulating protection member covering said semiconductor chip; and
- a heat transfer part provided so as to contact said protection member, said optical element assembly, and said mounting member;
- wherein said semiconductor chip is provided between said circuit board and said mounting member.
14. The optical data link according to any one of claims 1 to 10, further comprising:
- an electrically conductive cover covering said circuit board;
- a semiconductor chip mounted on said circuit board;
- an electrically insulating protection member covering said semiconductor chip; and
- a heat transfer part provided so as to contact said protection member, said optical element assembly, and said electrically conductive cover;
- wherein said circuit board has a thermal via in an area in which said semiconductor chip is mounted;
- wherein said heat transfer part is provided so as to contact said thermal via;
- said semiconductor chip is provided between said circuit board and said mounting member.
15. The optical data link according to claim 11, wherein

said heat transfer part is provided so as to contact an electric conductor connected with said semiconductor chip,

16. The optical data link according to claim 11, wherein said heat transfer part has electrical insulation preventing electrical conduction from occurring there-through. 5
17. The optical data link according to claim 12, wherein said heat transfer part exhibits flexibility to deform to conform to the shape of the protection member and optical element assembly when said heat transfer part is provided between said electrically conductive cover and said protection member and optical element assembly. 10 15
18. The optical data link according to claim 11, wherein said heat transfer part includes silicone gel material. 20
19. The optical data link according to claim 11, wherein said optical element assembly includes a semiconductor optical element, and wherein said semiconductor chip comprises circuit elements for driving said semiconductor optical element. 25
20. The optical data link according to claim 11, said protection member includes one of a package and a resin, said package housing said semiconductor chip therein, and said resin being mounted on said circuit board so as to cover said semiconductor chip therewith. 30 35

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Fig.1

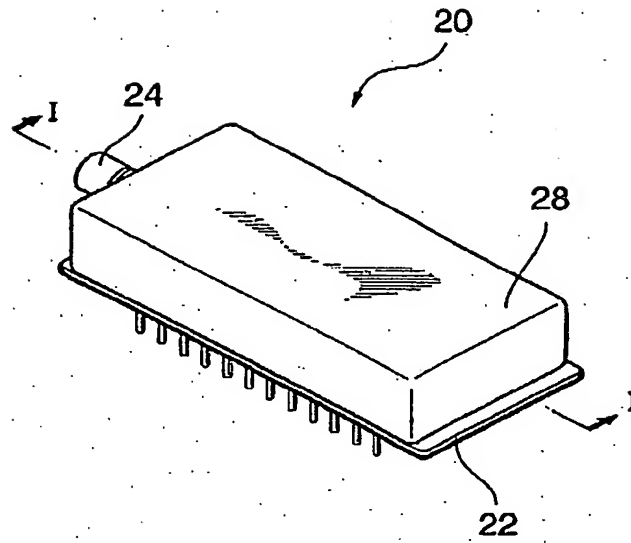


Fig.2

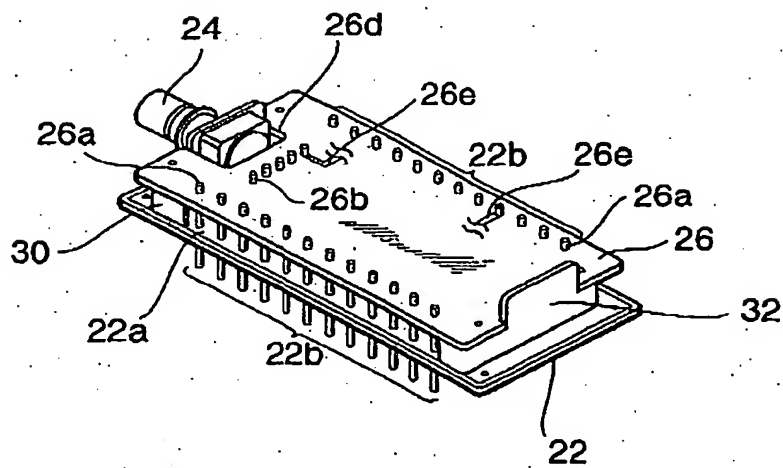


Fig.3

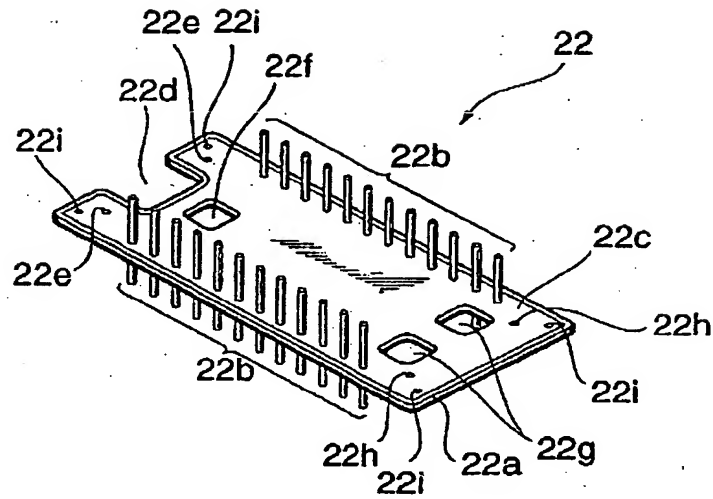


Fig.4

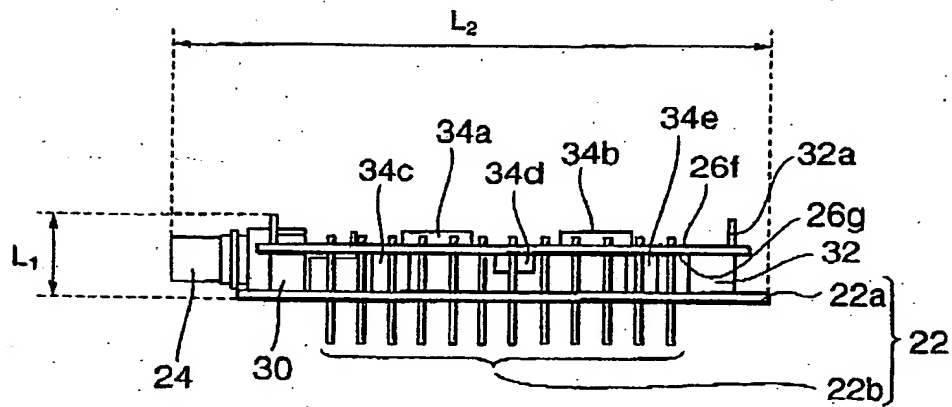


Fig. 5

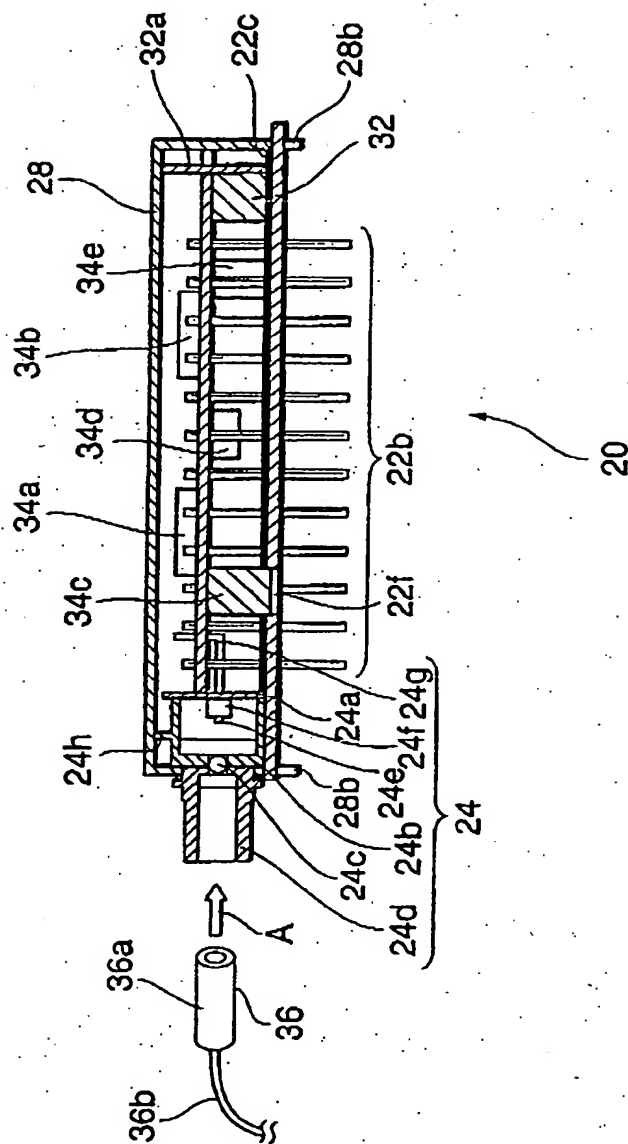


Fig. 6

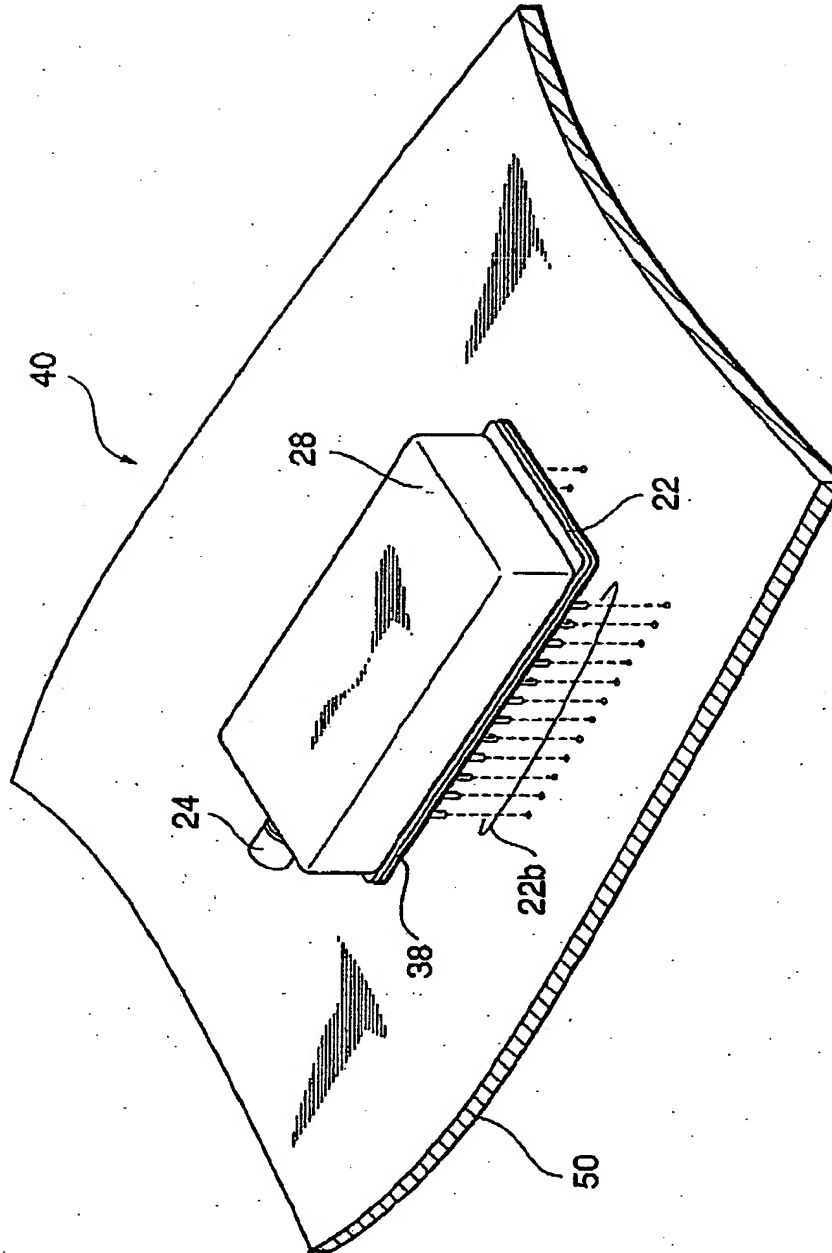
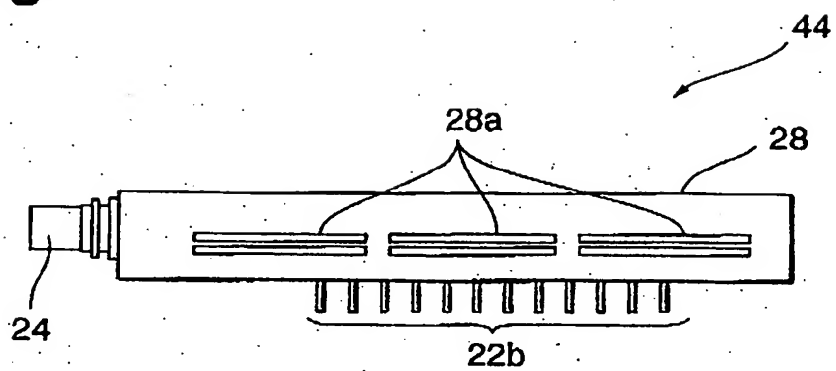


Fig.7



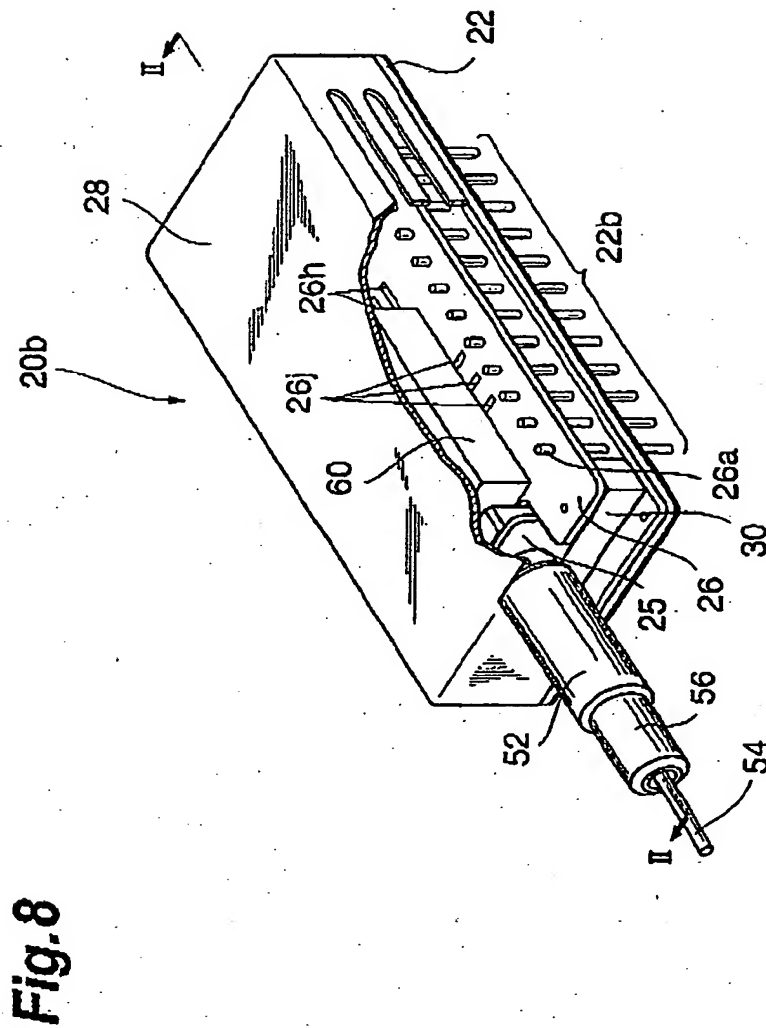


Fig.9

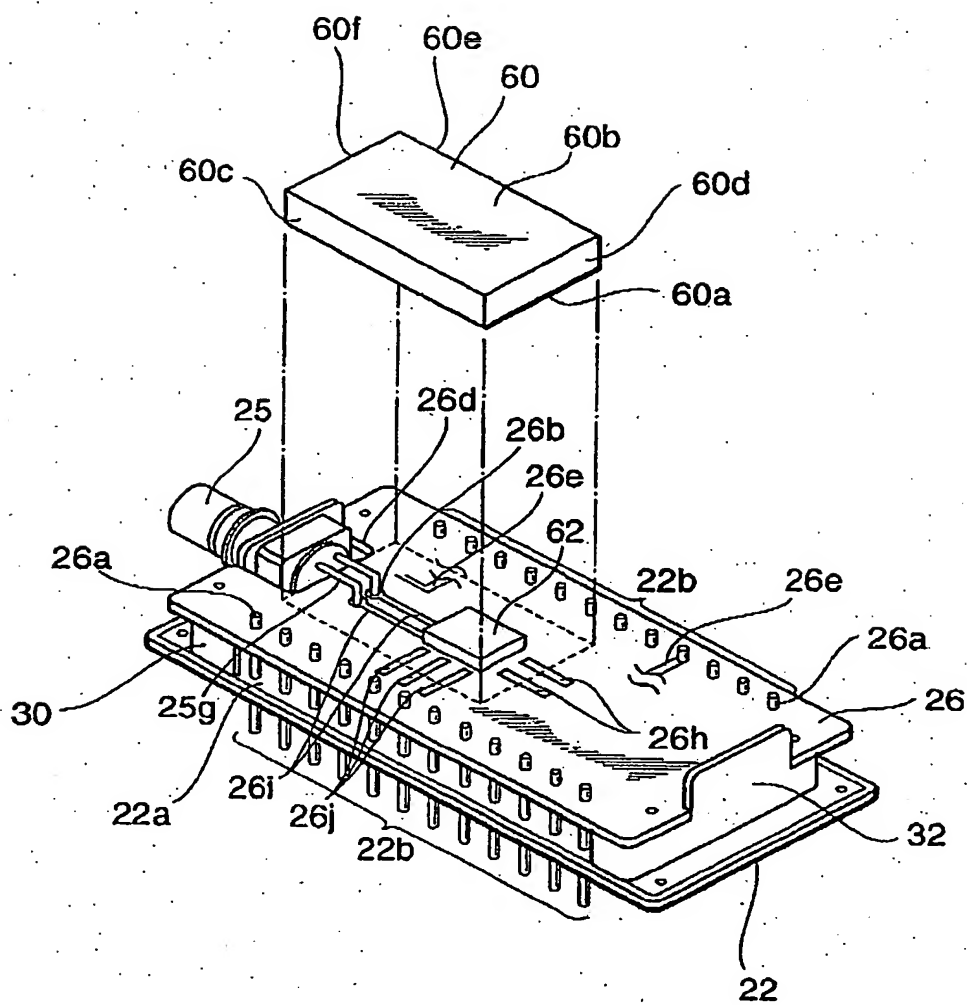


Fig.10

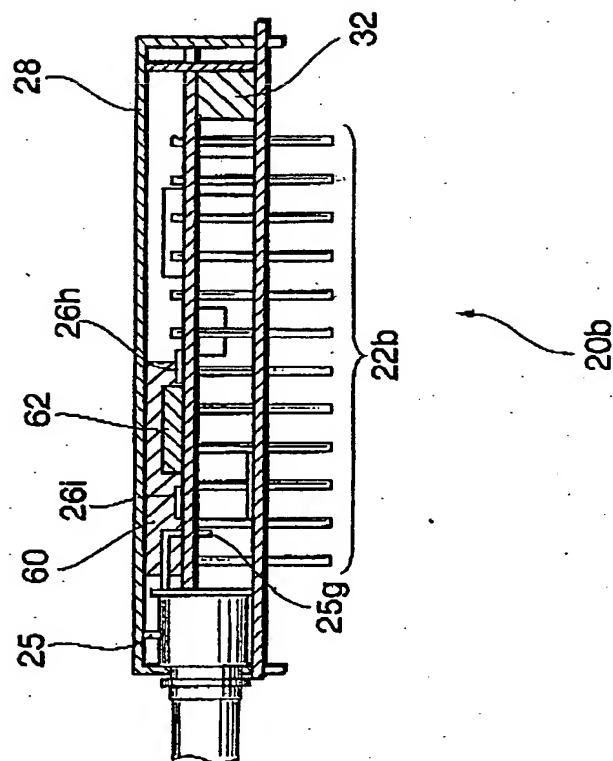


Fig.11

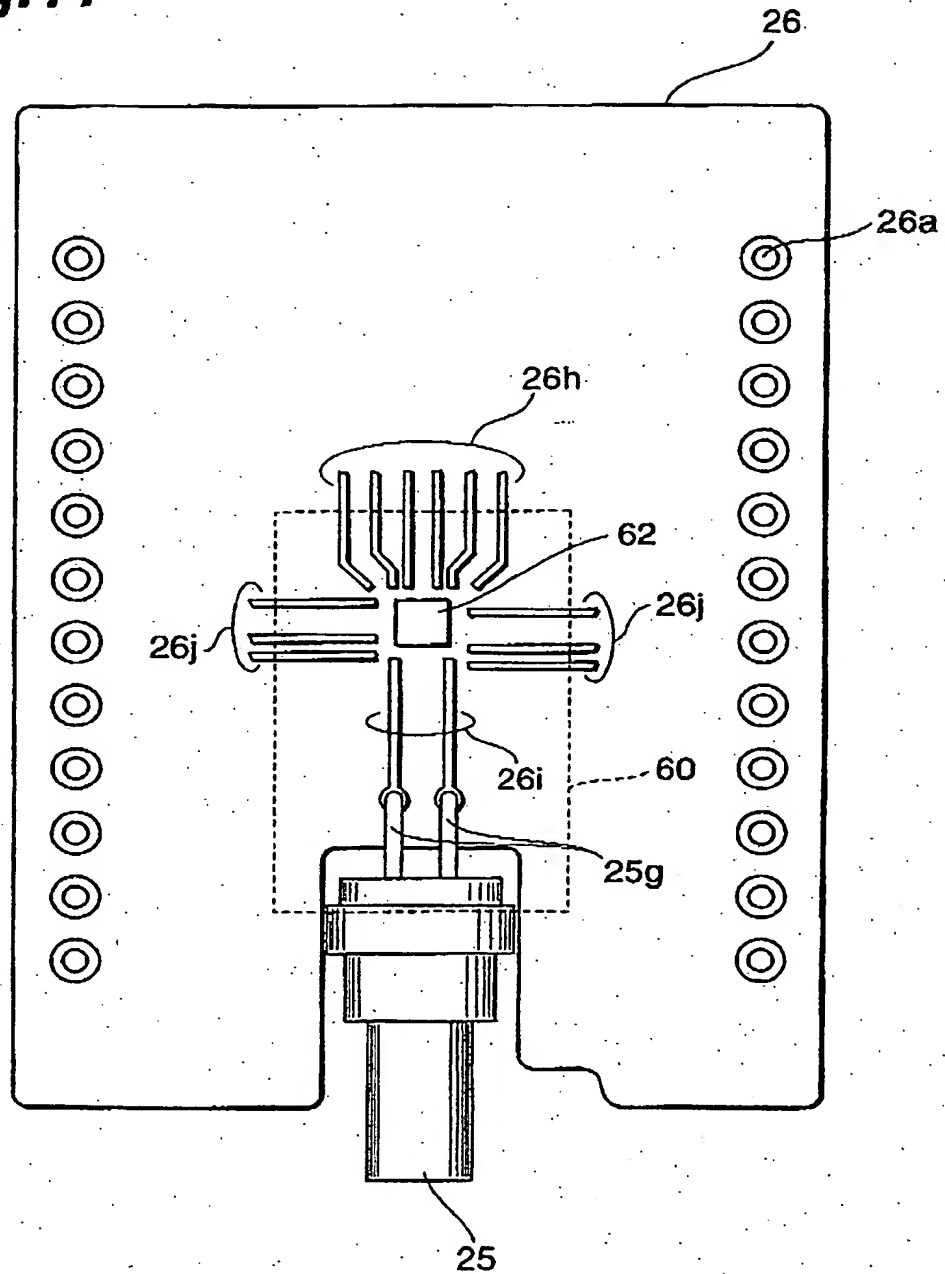


Fig.12A

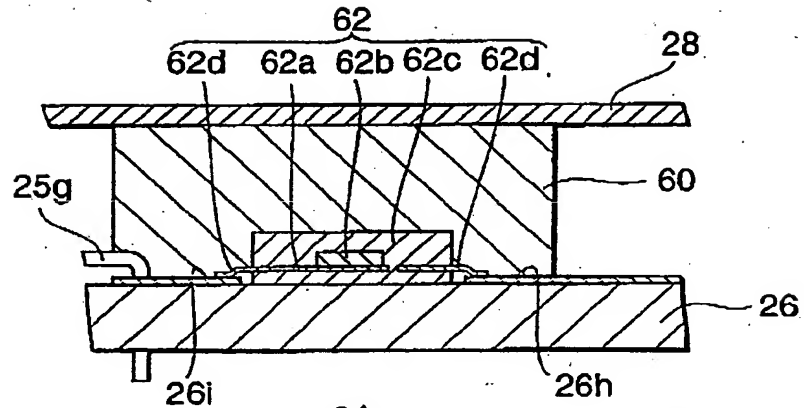


Fig.12B

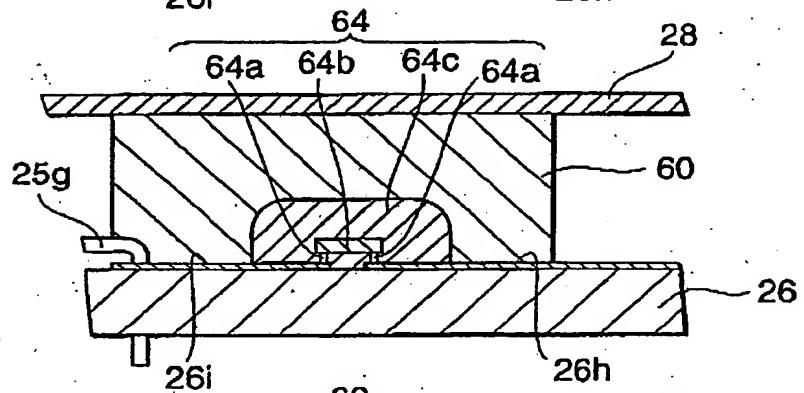


Fig.12C

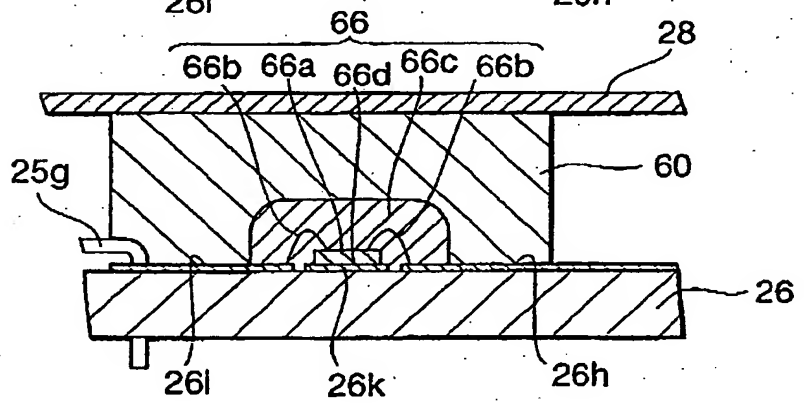


Fig.13

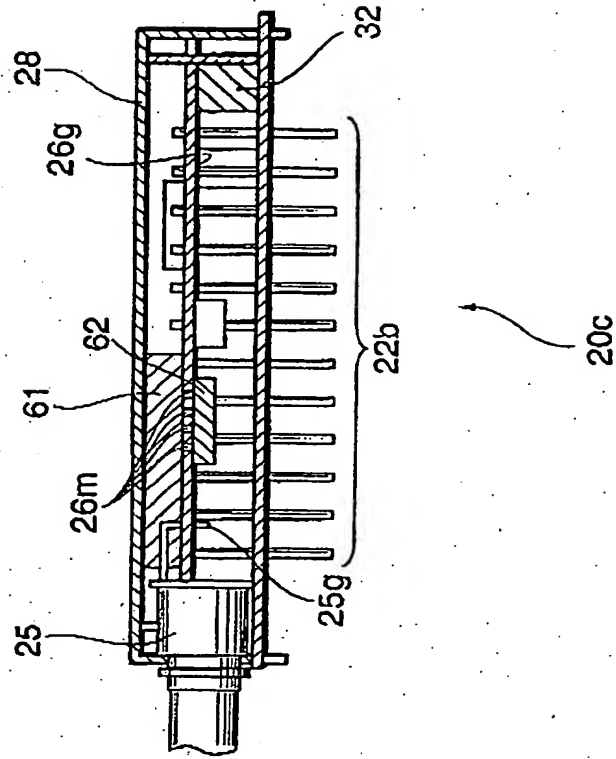
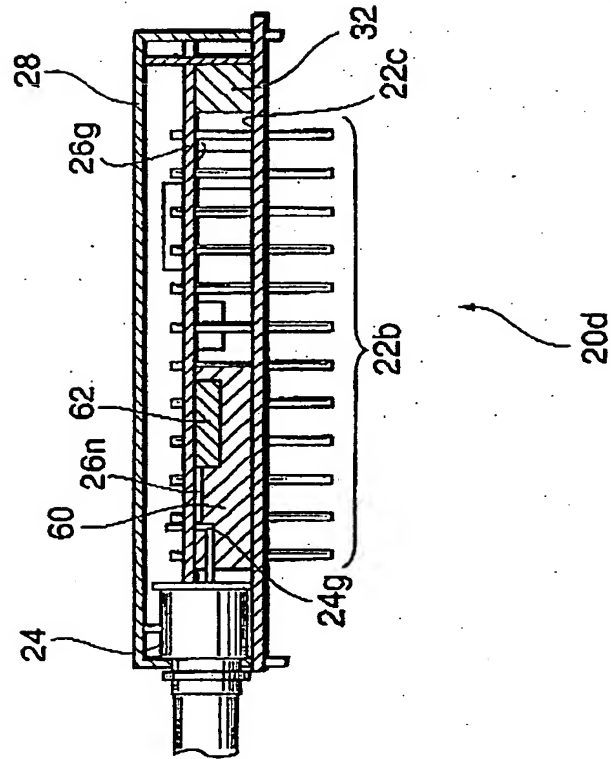


Fig.14



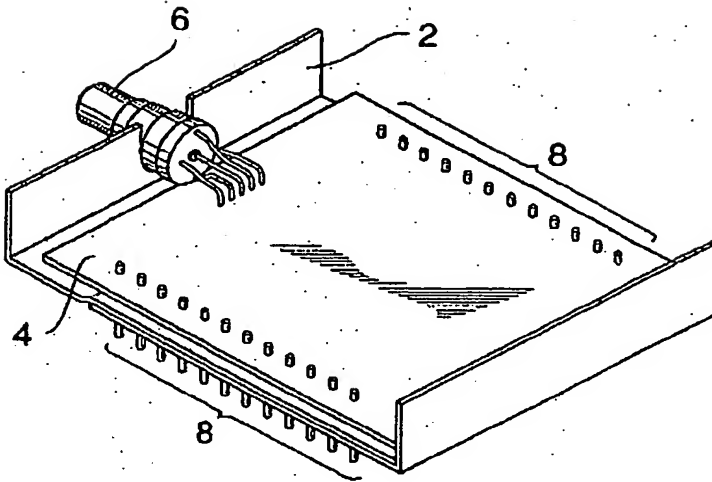


Fig. 15A

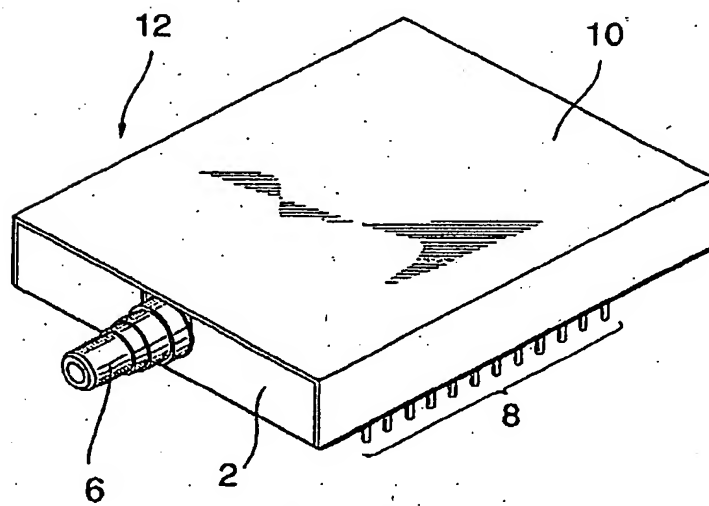


Fig. 15B

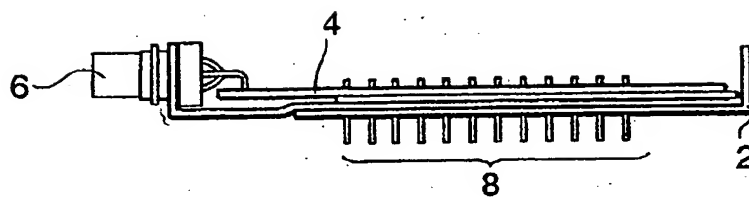


Fig. 16A

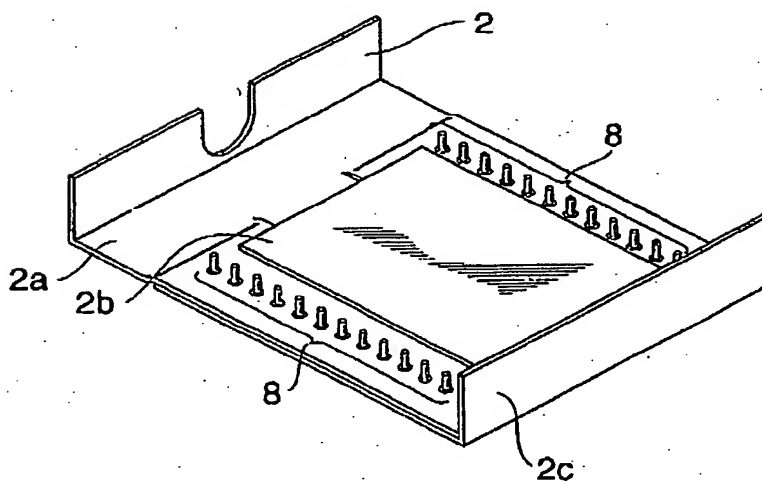


Fig. 16B

(19)



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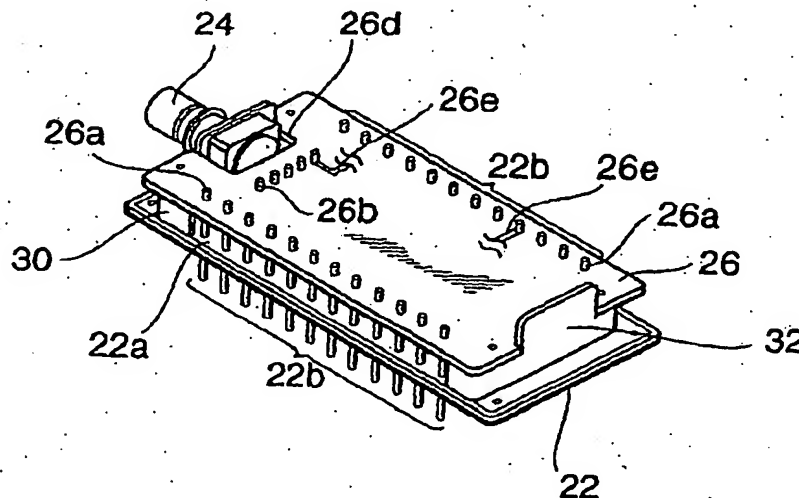
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(54) Opto-electronic module with printed circuit board (PCB)

(57) An optical data link 20 comprises a mounting member 22, an optical element assembly 24, a circuit board 26, and a spacer 32. The PGA substrate 22 has a substrate 22a and a plurality of electrically conductive pins 22b. The circuit board 26 has a pair of surfaces. Electronic components 34a to 34e are mounted on the pair of surfaces. The optical element assembly 24 in-

cludes a semiconductor optical element 24f. The semiconductor optical element 24f is connected to an electrically conductive layer on the circuit board 26. The spacer 32 functions so as to distance the circuit board 26 from the PGA substrate 22. Since the circuit board 26 is distanced from the PGA substrate 22, the electronic components 34a to 34e can be mounted on both surfaces of the circuit board 22.

Fig.2



European Patent
Office

EUROPEAN SEARCH REPORT

Application Number
EP 02 00 4306

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.7)
X	WO 94/01987 A (ERICSSON TELEFON AB L M) 20 January 1994 (1994-01-20) * abstract; figures 4a,4b,5a,5b *	1	G02B6/42
X	US 5 528 408 A (BACKHAUSS HENNING ET AL) 18 June 1996 (1996-06-18) * abstract; figures 5,6 *	1	
A	US 5 416 668 A (BENZONI ALBERT M) 16 May 1995 (1995-05-16) * abstract; figures 1,2 *	2	
A	EP 0 437 161 A (IBM) 17 July 1991 (1991-07-17) * abstract; figure 1 *	1-10	
A	EP 0 448 989 A (IBM) 2 October 1991 (1991-10-02) * abstract; figure 4 *	1-10	
A	EP 0 750 204 A (HITACHI LTD) 27 December 1996 (1996-12-27) * abstract; figure 7 *	1	TECHNICAL FIELDS SEARCHED (Int.Cl.7) G02B
The present search report has been drawn up for all claims			
Place of search The Hague		Date of completion of the search 10 November 2004	Examiner Faderl, I
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPO FORM 1503 03.92 (P04/C07)



European Patent
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Application Number

EP 02 00 4306

CLAIMS INCURRING FEES

The present European patent application comprised at the time of filing more than ten claims.

- ☐ Only part of the claims have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims and for those claims for which claims fees have been paid, namely claim(s):
- ☐ No claims fees have been paid within the prescribed time limit. The present European search report has been drawn up for the first ten claims.

LACK OF UNITY OF INVENTION

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

see sheet B.

- ☐ All further search fees have been paid within the fixed time limit. The present European search report has been drawn up for all claims.
- ☐ As all searchable claims could be searched without effort justifying an additional fee, the Search Division did not invite payment of any additional fee.
- ☐ Only part of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the inventions in respect of which search fees have been paid, namely claims:
- ☒ None of the further search fees have been paid within the fixed time limit. The present European search report has been drawn up for those parts of the European patent application which relate to the invention first mentioned in the claims, namely claims:

1-10



European Patent
Office

LACK OF UNITY OF INVENTION
SHEET B

Application Number

EP 02 00 4306

The Search Division considers that the present European patent application does not comply with the requirements of unity of invention and relates to several inventions or groups of inventions, namely:

1. claims: 1-10

An optical data link comprising a mounting member having a substrate with a plurality of holes, and a plurality of electrically conductive pins passing through said substrate; a circuit board having a pair of surfaces with an electrically conductive layer connected to at least one pin of said plurality of electrically conductive pins, each of said pair of surfaces mounting an electronic component; an optical element assembly, mounted on said mounting member, including a semiconductor optical element connected to said electrically conductive layer provided on said circuit board; and means for supporting said circuit board so as to be spaced apart from said mounting member, wherein said circuit board is provided such that one of said pair of surfaces thereof faces a main surface of said mounting member characterised by an electrically conductive cover, said cover covering said circuit board, and wherein said mounting member has an electrically conductive film on said main surface of said mounting member

2. claims: 11-20

An optical data link comprising a mounting member having a substrate with a plurality of holes, and a plurality of electrically conductive pins passing through said substrate; a circuit board having a pair of surfaces with an electrically conductive layer connected to at least one pin of said plurality of electrically conductive pins, each of said pair of surfaces mounting an electronic component; an optical element assembly, mounted on said mounting member, including a semiconductor optical element connected to said electrically conductive layer provided on said circuit board; and means for supporting said circuit board so as to be spaced apart from said mounting member; and a semiconductor chip mounted on said circuit board characterised by an electrically insulating protection member covering said semiconductor chip; and a heat transfer part provided so as to contact said protection member and said optical element assembly

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 02 00 4306

This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on
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10-11-2004

Patent document cited in search report		Publication date	Patent family member(s)	Publication date
WO 9401987	A	20-01-1994	AU 4517193 A	31-01-1994
			AU 4681697 A	05-02-1998
			BR 9306685 A	08-12-1998
			CN 1081297 A ,B	26-01-1994
			EP 0649590 A1	26-04-1995
			FI 950058 A	05-01-1995
			JP 8501657 T	20-02-1996
			NO 950038 A	05-01-1995
			SE 9202077 A	07-01-1994
			WO 9401987 A1	20-01-1994
US 5528408	A	18-06-1996	GB 2294125 A ,B	17-04-1996
			SG 33545 A1	18-10-1996
US 5416668	A	16-05-1995	DE 69408782 D1	09-04-1998
			DE 69408782 T2	02-07-1998
			EP 0652696 A1	10-05-1995
			JP 2854254 B2	03-02-1999
			JP 7183681 A	21-07-1995
EP 0437161	A	17-07-1991	US 5039194 A	13-08-1991
			DE 69027522 D1	25-07-1996
			DE 69027522 T2	23-01-1997
			EP 0437161 A2	17-07-1991
			JP 3218134 A	25-09-1991
			JP 7093603 B	09-10-1995
			US 5069522 A	03-12-1991
EP 0448989	A	02-10-1991	US 5005939 A	09-04-1991
			BR 9101185 A	05-11-1991
			CA 2036959 A1	29-06-1993
			DE 69114161 D1	07-12-1995
			DE 69114161 T2	30-05-1996
			EP 0448989 A1	02-10-1991
			JP 2828220 B2	25-11-1998
			JP 7030133 A	31-01-1995
EP 0750204	A	27-12-1996	JP 3472660 B2	02-12-2003
			JP 9138329 A	27-05-1997
			EP 0750204 A1	27-12-1996
			US 5675685 A	07-10-1997

EPO FORM P0439

For more details about this annex : see Official Journal of the European Patent Office, No. 12/82

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